### AT91SAM7xx Parallel Input/Output Controller (PIO)

- **0 = pull-up en**
- **1 = pull-up dis**

- **0 = input**
- **1 = output**

- **0 = peripheral en**
- **1 = PIO en**

- **0 = peripheral en**
- **1 = PIO en**

- **0 = push-pull**
- **1 = open drain**

- **0 = glitch dis**
- **1 = glitch en**

- **0 = PIO_ODSR should be written only using PIO_SODR and PIO_CODR**
- **1 = PIO_ODSR should be written directly**

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**Image Description:**

The diagram illustrates the PIO controller's operation and configurations. It shows a network of switches and logic gates representing the control and status registers. The diagram highlights the control of input/output statuses, peripheral enable, and various output modes such as pull-up and push-pull. It also includes indicators for glitch detection and edge detection, along with PIO interrupt handling.